Amendments to the Specification

Please replace paragraph **[0049]** with the following replacement paragraph:

[0049] The operation frequency of the control and synchronization unit 124 is synchronized with the PLL bandwidth. For example, in an embodiment in which the antenna array is implemented as a horizontal planar antenna array with four antenna elements (e.g., as shown in Fig. 3), if the PLL bandwidth is between 20.. 25 Hz, then the operation period of the control and synchronization unit is equal to 0.8 ms. As described above, the control and synchronization unit 124 controls switch 116 to cyclically ~~connects~~ connect antenna elements AE_1 104, AE_2 106 ... AE_m 108 to RF cable 122 and the single signal processing path. Simultaneously, the phase shift correction module 134 of the control and synchronization unit 124 generates the phase shift correction signal which is output to the phase shifter 404. The phase shift correction signal is synchronously generated for the particular antenna element of the antenna array that is currently connected to the signaling path. Thus, for example, at the moment when the signal from antenna element AE_1 104 is connected to the single signal processing path, and is being processed by the satellite channel processors, the phase shift correction signal generated for AE_1 104 is provided to the phase shifter 404 of each of the satellite channel processors 400.